

**ABSTRACT**

A distributor box is provided for microprocessor, memory and logic components and/or power semiconductor components, with a housing comprising a lower housing compartment and at least one upper housing compartment, which can be connected to the lower compartment in a tightly sealed, firm manner to provide a high degree of protection. At least one transfer pin-and-socket connector is provided comprising a first and a second connector part, the first connector part of which is fixedly mounted in an upper housing compartment in such a way that when this upper compartment is set onto the lower compartment, the first connector part is brought into electrical contact with a second connector part of the transfer pin-and-socket connector, which fits together with the first part and is fixedly mounted in the lower housing compartment. In the lower housing compartment are mounted connecting devices or external terminal arrangements such as terminal strips or the like, so that a set of cables can be connected to at least the associated second pin-and-socket connector part. The lower housing compartment comprises a T-shaped, high tension cabling between two external connecting devices and one external terminal arrangement for a hybrid cable on the lower housing compartment. The lower housing compartment also comprises a T-shaped cabling for a field bus between two external terminal arrangements on the lower housing compartment and at least one second pin-and-socket connector part. In at least one upper housing compartment there is mounted an electronic circuit that can be electrically connected to a control bus by way of a first and a second pin-and-socket connector part. The lower housing compartment also comprises an external means of connecting a hybrid cable with hybrid pin-and-socket connector part that incorporates high tension and low tension leads, the high tension leads being connectable to at least one electronic circuit in an upper housing compartment.

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